PCI to PMC (PCI Mezzanine Card) Adapter

Designed by Engineers for Engineers

PCI to PMC Adapter with integrated logic, supporting debugging and verification PMC cards by plugging in to the standard PCI bus based computer. The Adapter is a complete debugging platform that allows easy access to logic signals, overcurrent protection, and hot-swap capability.

Features:

- Supports single PMC board (connectors PN1, PN2)
- Supports 5V and 3.3V signaling environments as well as live insertions for the plug-in PMC board
- Supports external power supply for the generation of all PCI voltage levels
- Power monitoring and current limiting for PMC test connector
- Master I2C and SPI operation
- Implements Universal FPGA/CPLD ByteBlaster functionality for the different types of FPGA (like Xilinx, Altera, Lattice)
- Equipped with a test point header array with all PCI bus signals for logic analyzer hookup
- IEEE-1149.1 JTAG Boundary Scan port is available to support system and board level testing
- Patented mechanical design allows easy access to the connector on the UUT in any computer cases (U.S. Patent 7,186,145)
- Supports 2U and standard computer cases

Software

- Software for Linux, FreeBSD, Windows OS.

Built-in
Universal ByteBlaster, supporting programming wide range of FPGA/CPLD from all major vendors

Built-in
I2C/SPI control logic

Supports
hookup of high density Logic Analyzer probes for all PCI signals

Easily
plug-in / plug-out test PMC board with cables in any standard computer case (US Patent 7,186,145)

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